

BOSE INSTITUTE KOLKATA

Tender No.	CAPSS/15/40(ALICE-SD)/13-14
Tender Date	15/07/2013
Tender Type	OPEN

Tender Title	FPGA Development kit
Specification	<ul style="list-style-type: none"> • LX45 FPGA, 324-pin BGA package • 128Mbyte DDR2 16-bit wide data • 10/100/1000 Ethernet PHY • On-board USB2 ports for programming & data transfer • USB-UART and USB-HID port (for mouse/keyboard) • Two HDMI video input ports & two HDMI output ports • AC-97 Codec with line-in, line-out, mic, & headphone • Real time power monitors on all power rails • 16Mbyte x4 SPI Flash for configuration & data storage • 100MHz CMOS oscillator • 48 I/O's routed to expansion connectors <p>GPIO includes</p>
Quantity	TWO

Last Date & Time for submission	31/07/2013 upto 2.00 pm
Date & Time for opening bids	31/07/2013 at 3.00 p.m.
Submission of Tender (address)	CAPSS, Salt Lake, Block EN 80, Kol. 91
Venue of bid opening	CAPSS, Salt Lake, Block EN 80, Kol. 91
For any query the interested bidders may contact (Dept./Section/Div./Unit)	033 2569 3109/3123

General Terms & Conditions

Warranty	12 months from the date of satisfactory installation
Payment terms	Payment will be made after complete delivery of the instrument in good condition and satisfactorily installation.
Delivery schedule	Within 30 days from the date of receiving order & if any defect of the supplied item is found, it should be replaced immediately from your side.
Bid security (earnest money deposit) if applicable	NA

Submission of Performance Bank Guarantee (PBG), if applicable	NA
Any other information (if applicable)	

Name of the instrument and submission of tender should be mentioned on the envelop positively

Director, Bose Institute reserves the right to accept or reject any or all tenders either in part or in full. The reasons for rejecting the tender of a prospective bidder will be disclosed only when enquiries are made.

Registrar
16.7.2013