BOSE INSTITUTE KOLKATA

Tender No.	CAPSS/15/38/ALICE-SD/13-14
Tender Date	15/07/2013
Tender Type	OPEN

Tender Title	FPGA Evaluation Board		
Specification	FPGA:		
	ROHS Evaluation kit including the XC7VX485T-2FFG1761 FPGA Chip		
	<u>Configuration</u>		
	 Onboard JTAG configuration circuitry to enable configuration over USB JTAG header provided for use with Platform Cable USB II 128MB (1024Mb) Linear BPI Flash for PCIe® Configuration 16MB (128Mb) Quad SPI Flash 		
	Memory		
	 1GB DDR3 SODIMM 800MHz / 1600Mbps 128MB (1024Mb) Linear BPI Flash for PCIe Configuration SD Card Slot 		
	Communication & Networking		
	 Gigabit Ethernet GMII, RGMII and SGMII SFP+ transceiver connector GTX port (TX, RX) with four SMA connectors UART To USB Bridge PCI Express x8 gen2 Edge Connector (lay out for Gen3) 		
	Display		
	 HDMI Video OUT 2 x16 LCD display 8X LEDs 		

	Expansion Connectors FMC Connectors 	
	 <u>Clocking</u> Fixed Oscillator with differential 200MHz output Programmable Oscillator with 156.250 MHz as the default output Differential SMA clock input Differential SMA GTX reference clock input Jitter attenuated clock <u>Control & I/O</u> 5X Push Buttons 8X DIP Switches <u>Power</u> 12V wall adapter or ATX Voltage and Current measurement capability <u>Debug & Analog Input</u> 8 GPIO Header, 9 pin removable LCD Analog Mixed Signal (AMS) Port 	
Quantity	ONE	

Last Date & Time for submission	31/07/2013 upto 2.00 pm
Date & Time for opening bids	31/07/2013 at 3:00 p.m.
Submission of Tender (address)	CAPSS, Salt Lake, Block EN 80,
	Kolkata 91
Venue of bid opening	CAPSS, Salt Lake, Block EN 80
	Kolkata 91
For any query the interested bidders may	033 2569 3109/3123
contact (Dept./Section/Div./Unit)	

General Terms & Conditions			
Warranty	12 months from the date of satisfactory		
	installation		
Payment terms	Payment will be made after complete delivery		
	of the instrument in good condition and		
	satisfactorily installation.		
Delivery schedule	Within 30 days from the date of receiving		
	order & if any defect of the supplied item is		
	found, it should be replaced immediately from		
	your side.		
Bid security (earnest money deposit) if	NA		
applicable			
Submission of Performance Bank	NA		
Guarantee (PBG), if applicable			
Any other information (if applicable)			

Name of the instrument and submission of tender should be mentioned on the envelop positively

Director, Bose Institute reserves the right to accept or reject any or all tenders either in part or in full. The reasons for rejecting the tender of a prospective bidder will be disclosed only when enquiries are made.

> Registrar 16.7.2013